

FIG. 1

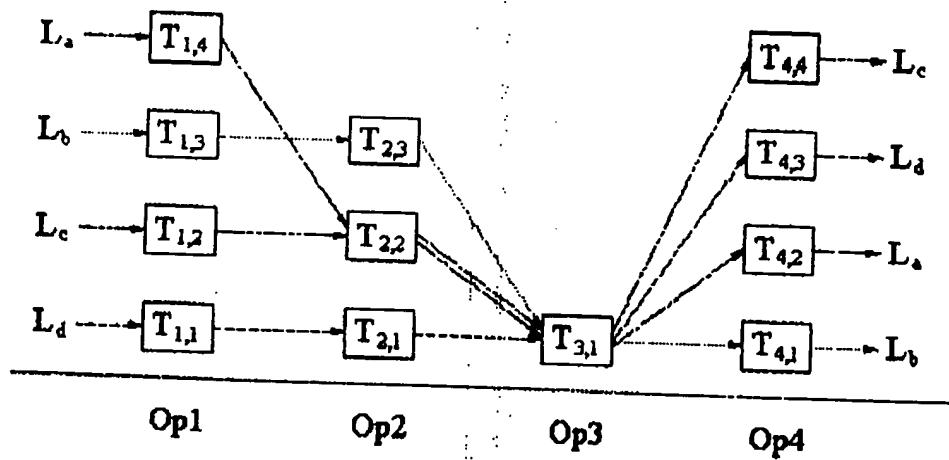


FIG. 2

Lot: L _a		Process Flow=PF101					D _a
Ld _a	operation	Op1	Op2	Op3	Op4	---	
Yd _a	tool	T _{1,1}	T _{2,2}	T _{3,1}	T _{4,2}	---	
Yd _a	wafer	W _{1,1}	W _{1,2}	W _{1,3}	W _{1,4}	---	
Yd _a	yield	Y _{1,1}	Y _{1,2}	Y _{1,3}	Y _{1,4}	---	

FIG. 3

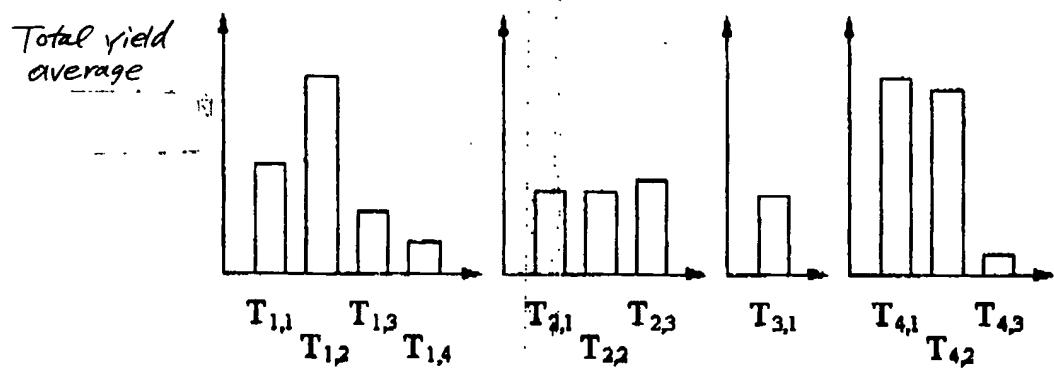


FIG. 4